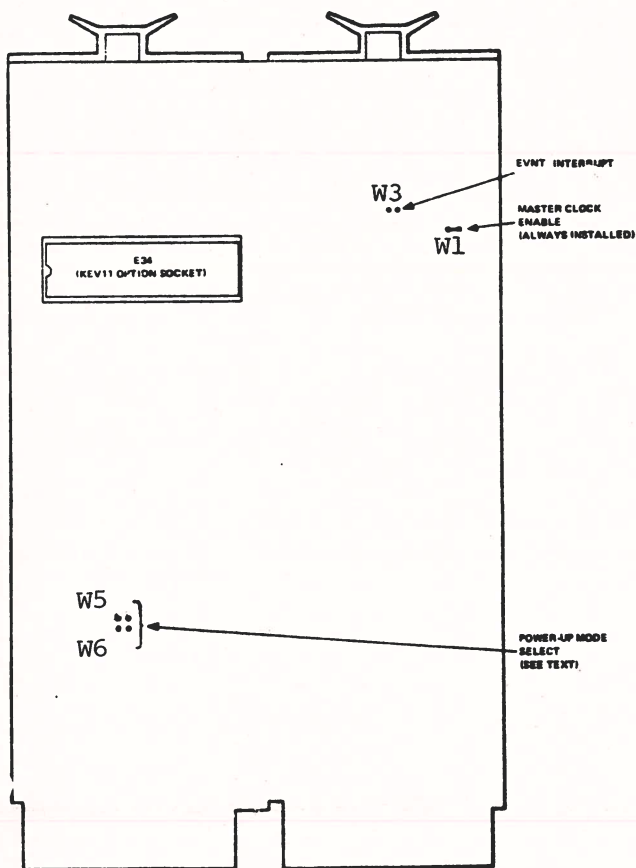


KD11-HA Configuration Guide (M7270)



R = Jumper Removed
I = Jumper Inserted

1. Power-Up Mode Select

Mode	Jumpers		Mode Select
	W6	W5	
0	R	R	PC at 24 PS at 26
1	R	I	ODT Microcode
2	I	R	PC at 173000
3	I	I	Special Microcode (not implemented)

2. Event Line Interrupt

Mode	W3
Enabled	R
Disabled	I

3. Misc.

Jumper W1 - Master Clock Enable
W1 must always be installed for processor operation. It is provided for manufacturing test purposes and normally should not be removed.

Notes:

- 1) The KD11-HA contains no on-board memory.
- 2) The KD11-HA cannot perform memory refresh. All RAM memory in the system must have on-board refresh, or the REV11 must be used for bus refresh. However, if the REV11 is used to provide memory refresh, it must be Circuit Schematic (CS) level J or higher (the CS level designator is stamped on the back of the module handle).

Note that failure to meet the above condition will cause the REV11 to hang up the system.

- 3) The KD11-HA has a built in "wake up" circuit, to avoid the need for power sequencing logic. This circuit automatically powers-up the KD11-HA through the selected power-up mode after +5 volts has been applied.

In order for the "wake up" circuit to operate reliably, the +5 and +12 volt supplies must reach their required voltage levels within 50 ms of turn-on. Note that this "wake up" circuit does not provide power-fail detection or power down sequencing. If power-fail is required, a KPV11 Power Sequence Module must be used.

To disable the wake-up circuit, which is recommended if power-fail is used, remove capacitor C81 (39 µf, 10V capacitor).