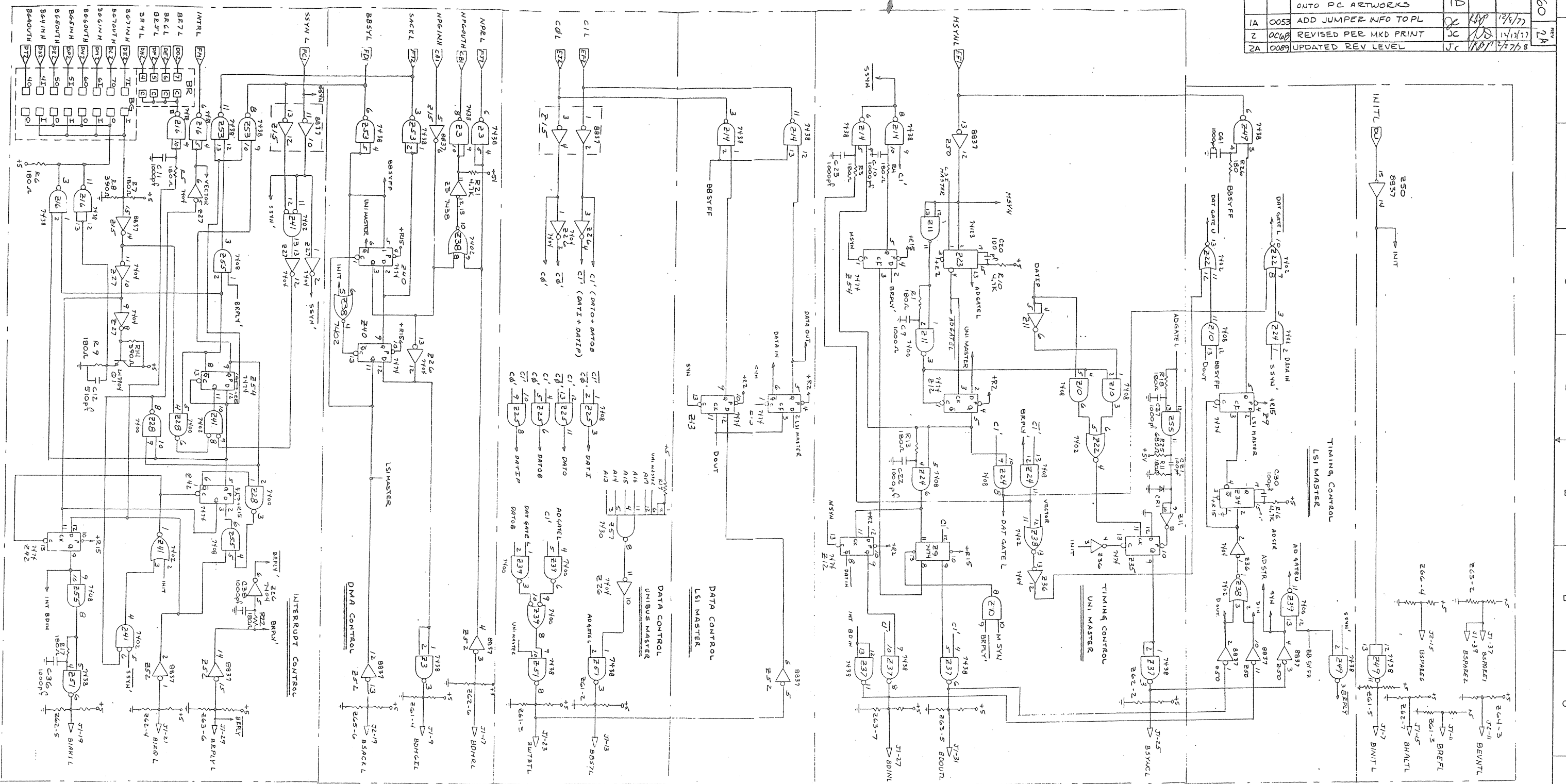


THESE DRAWINGS AND SPECIFICATIONS ARE THE PROPERTY OF adac corporation AND SHALL NOT BE REPRODUCED OR COPIED OR USED AS THE BASIS FOR MANUFACTURE OR SALE OF APPARATUS WITHOUT EXPRESSLY WRITTEN AUTHORIZATION.

REVISIONS				REV	DATE
REV	ECO	DESCRIPTION	DWN	APPLS	DATE
0A	-	REVISED CIRCUITRY DELETED R12, C19 ADDED R21, R22, R23, R24, C39, C40	JB		
1	0028	INCORPORATE REV DA CHANGES ONTO PC ARTWORKS	JB		
1A	0053	ADD JUMPER INFO TO PL	JB		12/17/77
2	0069	REVISED PER MKD PRINT	JC		12/17/77
2A	0089	UPDATED REV LEVEL	JC		12/26/78



SPARE GATES:
Z28 7400 12, 13, 11
Z27 7404 3, 4

TYPICAL ALL J1 & J2 SIGNALS
+5V
100Ω
390Ω
REPRESENTS ONE PIN OF 8 PIN SIP RESISTOR NETWORK ADAC # 23-00002 (PIN 1 = GND) (PIN 8 = +5V)

+5V
12 R15, R2

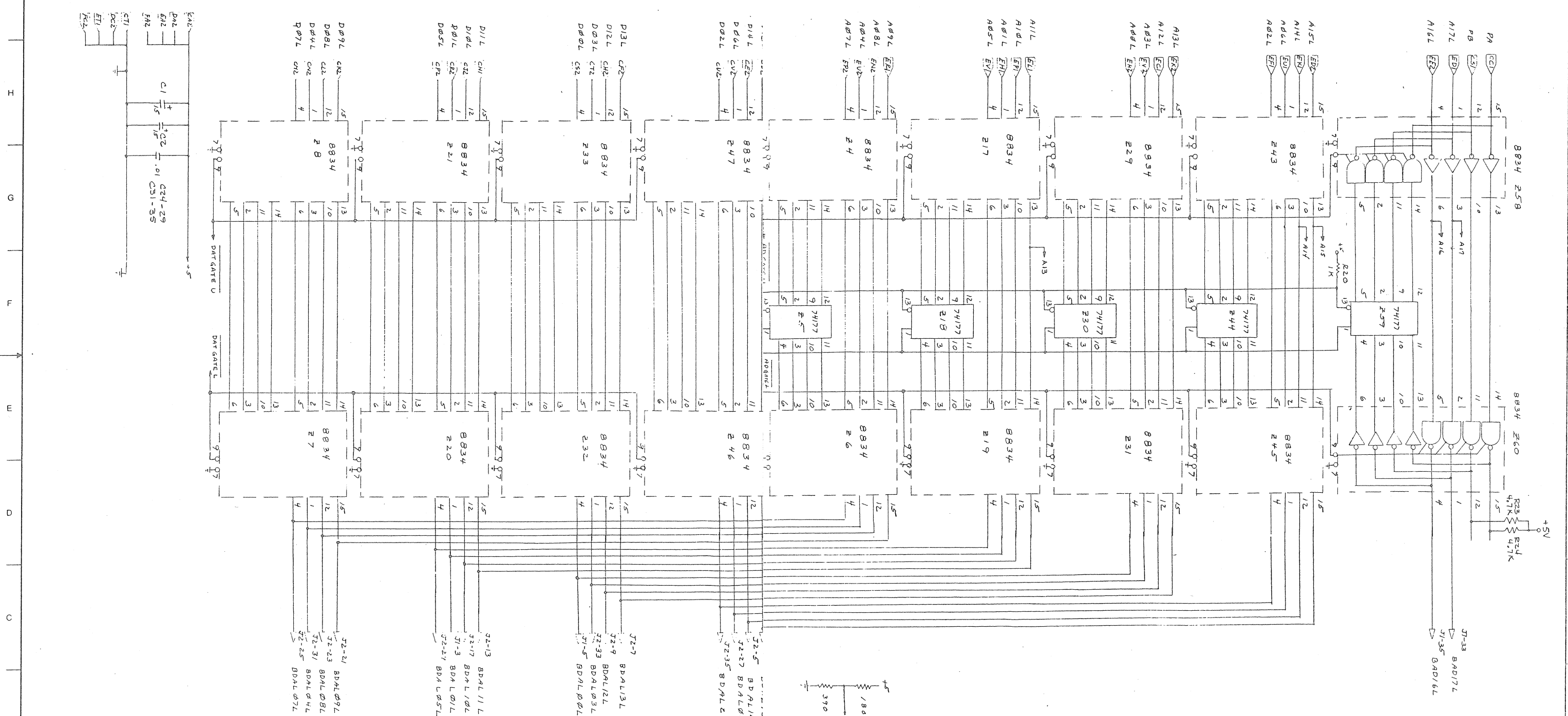
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES UNLESS OTHERWISE SPECIFIED ARE: FRACTIONS DECIMALS ANGLES	DESIGNER	DATE
MATERIAL	ENGINEER	1/15/77
FINISH	RELEASED	
APPLICABLE SPECIFICATIONS	WEIGHT	
NEXT ASSY	USED ON	
APPLICATION		

adac corporation	
MODEL 1900	
LSI-11 TO UNIBUS TRANSLATOR	
SCHEMATIC	
CODE IDENT NO.	SIZE
D	5-10060
SCALE	ACCOUNT NO.
SHEET 1	OF 2

THESE DRAWINGS AND SPECIFICATIONS ARE THE PROPERTY OF adac corporation AND SHALL NOT BE REPRODUCED OR COPIED OR USED AS THE BASIS FOR MANUFACTURE OR SALE OF APPARATUS WITHOUT EXPRESSLY WRITTEN AUTHORIZATION.

REVISIONS			
REV	ECD	DESCRIPTION	DATE
1		SEE SHEET 1 FOR REVISIONS	

SIZE D
5-10060
REV 2A



180
390
ONE FOR EACH
J1 J2 SIGNAL
33 PLACES
BERKMAN 768-F-100190

UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES TOLERANCES ON FRACTIONS DECIMALS ANGLES		DRAWN <i>WJS</i>	DATE <i>1/17/77</i>
MATERIAL	FINISH	CHECKED	DESIGNER
APPLICABLE SPECIFICATIONS	WEIGHT	ENGINEER	RELEASED
NEXT ASSY	USED ON	APPROVED <i>WJS</i> <i>1/17/77</i>	
APPLICATION		adac corporation	
		MODEL 1900	
		LSI-11 TO UNIBUS TRANSLATOR	
		SCHEMATIC	
CODE IDENT NO	SIZE	REV	
	D 5-10060	2A	
SCALE	ACCOUNT NO.	SHEET 2 OF 2	