

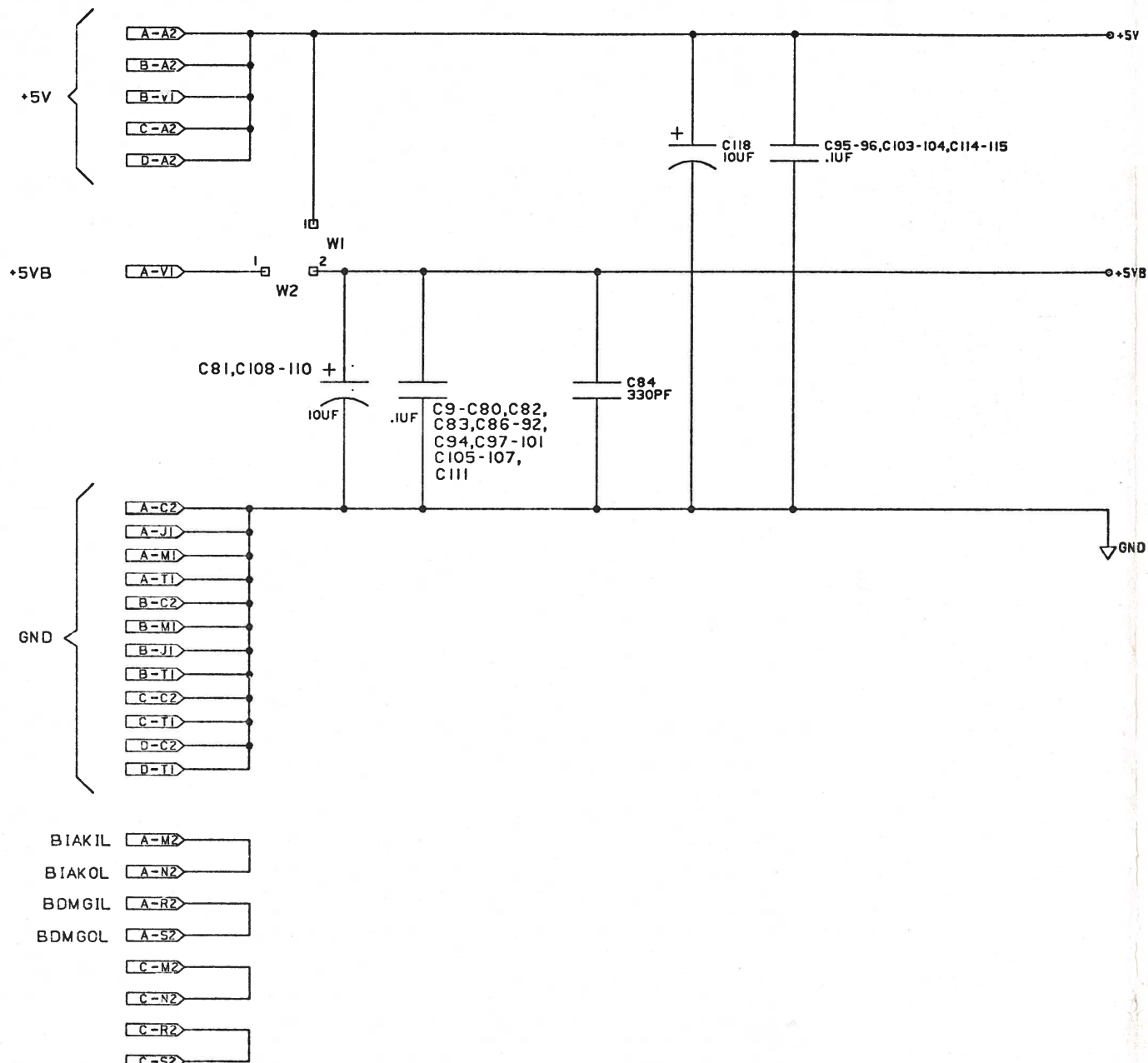
REVISIONS			
REV	DESCRIPTION	DATE	APP
A	PCN 31908		
B	PCN 32043, 32043A, 32043B		
C	ECN 32292		
D	ECN 32624		

PENDING ECN 32610

PRODUCTION

MAR 22 1985

RELEASED

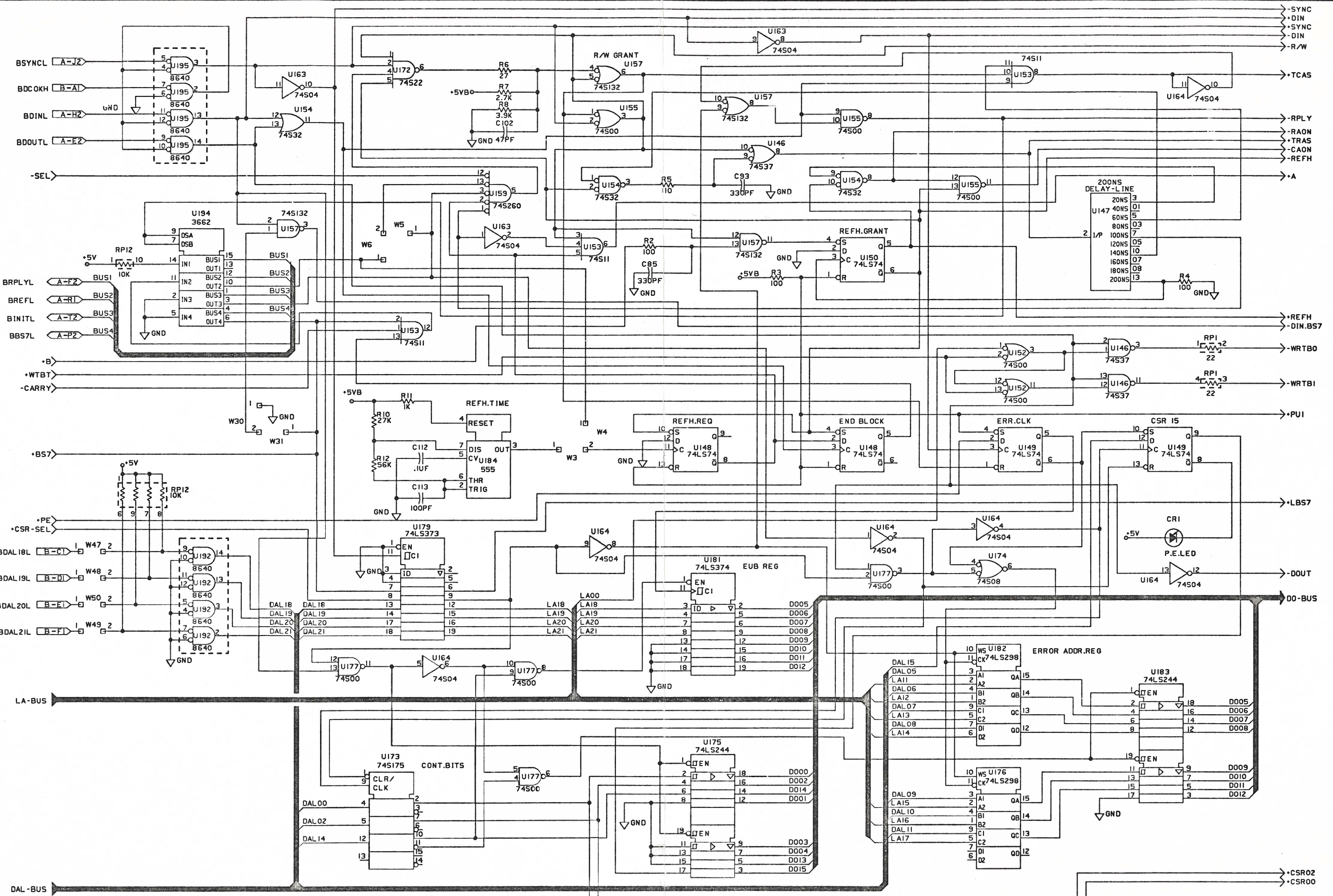


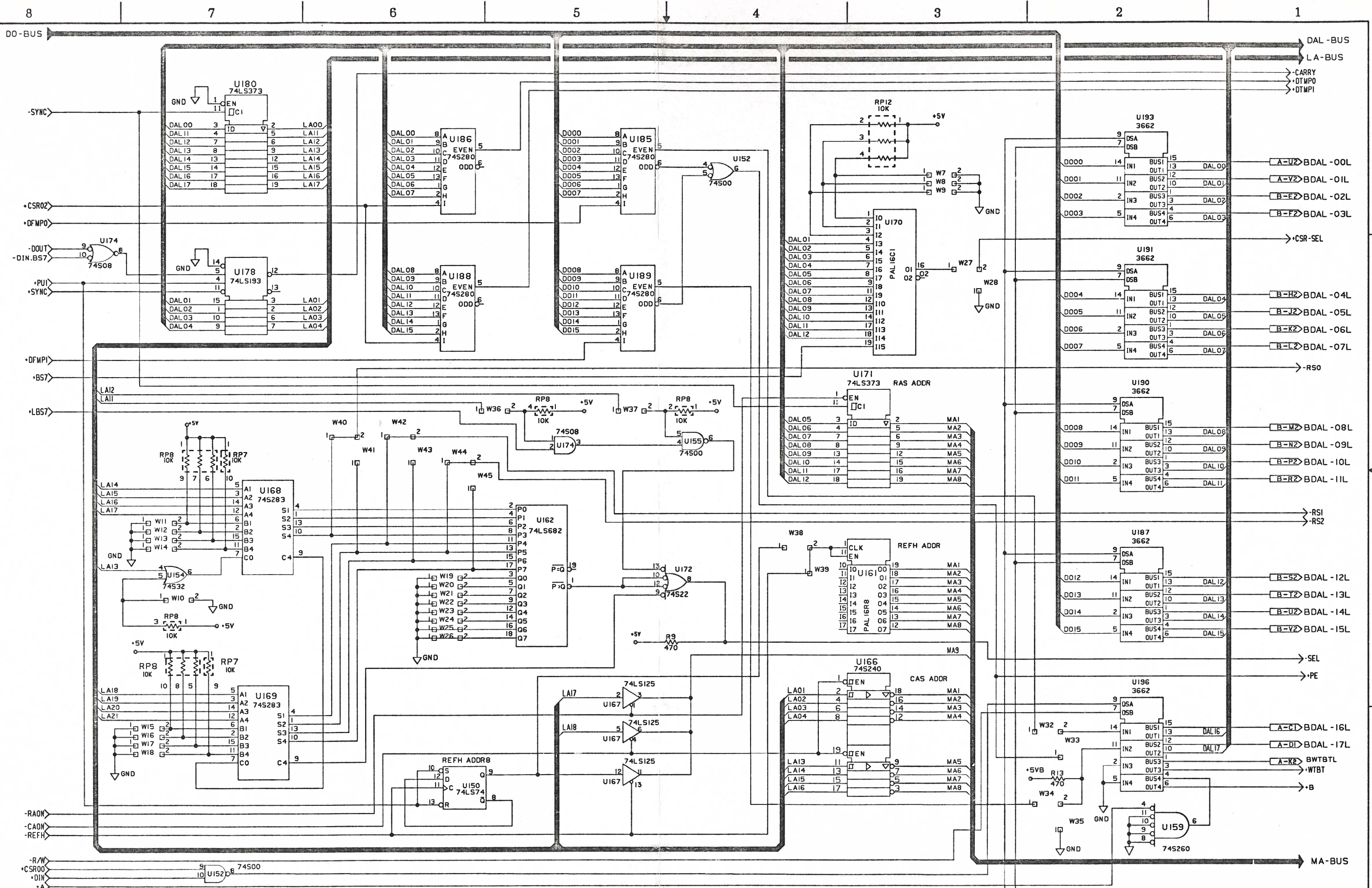
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74LS74	14		7	U149
74S08	14		7	U174
74S00	14		7	U177
74S280	14		7	U185-186, U188-189
74S175	16		8	U173
74LS298	16		8	U176, U182
74LS193	16		8	U178
74S283	16		8	U168-169
3662	16		8	U187, U190-191, U193, U196
74LS682	20		10	U162
PAL16C1	20		10	U170
74LS244	20		10	U175, U183
74LS373	20		10	U179, U180
74LS374	20		10	U181
8640	8		1	U192
200NS DELAY	-		1,14	U147
74S37		14	7	U146, U151, U165
74LS74		14	7	U148, U150
74S00		14	7	U152, U155
74S04		14	7	U163-164
74S11		14	7	U153
74S32		14	7	U154
74S132		14	7	U157
74S260		14	7	U159
74LS125		14	7	U167
74S22		14	7	U172
3628		20	10	U145, U156, U160
PAL16R8		20	10	U161
74S240		20	10	U166
74LS373		20	10	U171
PAL10H8		20	10	U158
4164		8	16	U1-144
3662		16	8	U194
555		8	1	U184
8640		8	1	U195
4164			1-16	U197 (SPARE)

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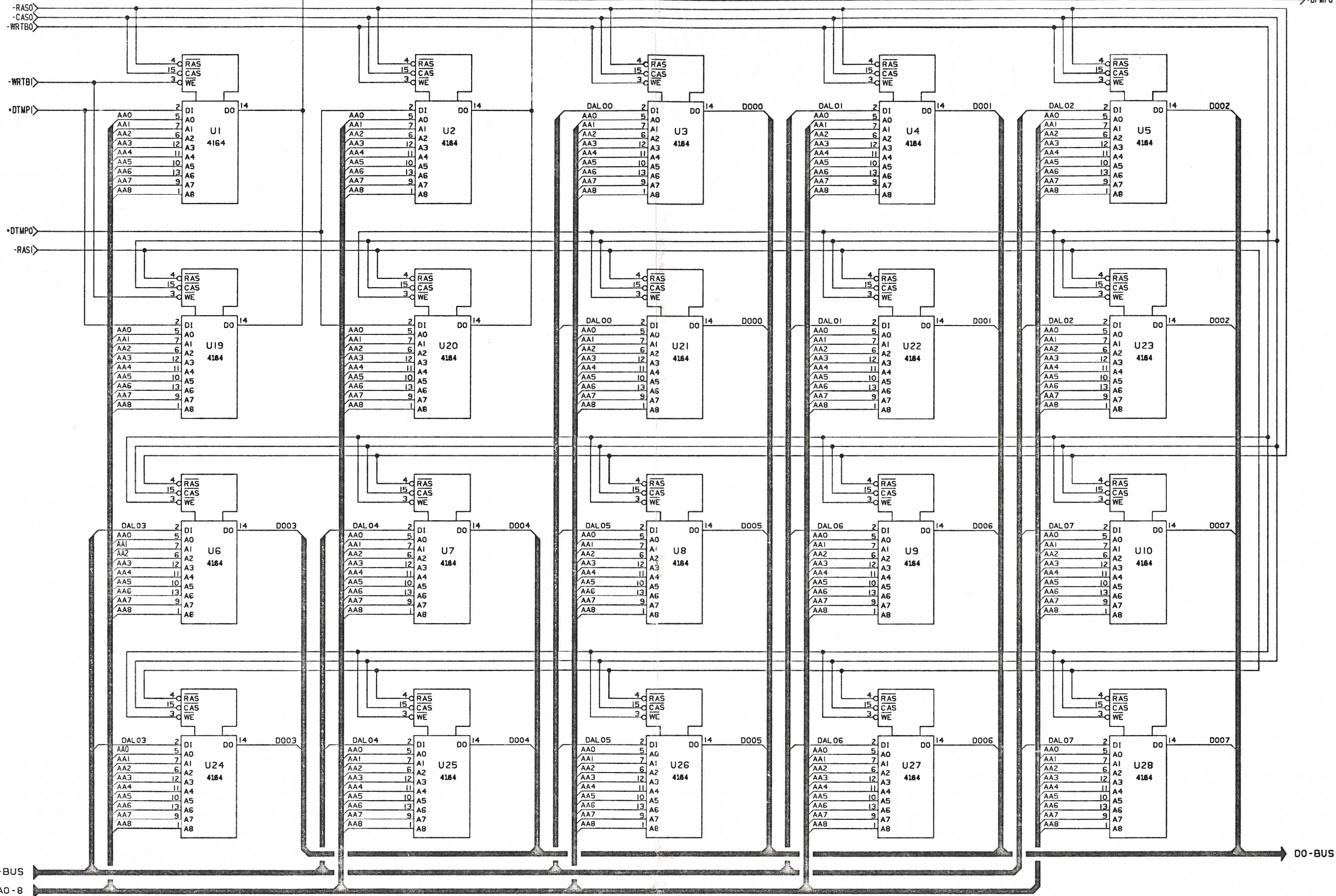
LAST USED	NOT USED	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ARE:	APPROVALS	DATE	NATIONAL SEMICONDUCTOR CORP.
U196		DECIMALS ANGLES FRACTIONS	DOANH-DUONG	11-14-83	
R13	R1	.XXX*	CHECKED		NS23S MEMORY MODULE
RP12		MATERIAL	APPROVED		
C118	C1-B, C91, C116-C117	FINISH	APPROVED		DRAWING NO. 870110014-000 SCALE NONE SHEET 1 OF 11
CR1			DO NOT SCALE DRAWING		

CAD PRODUCED DRAWING





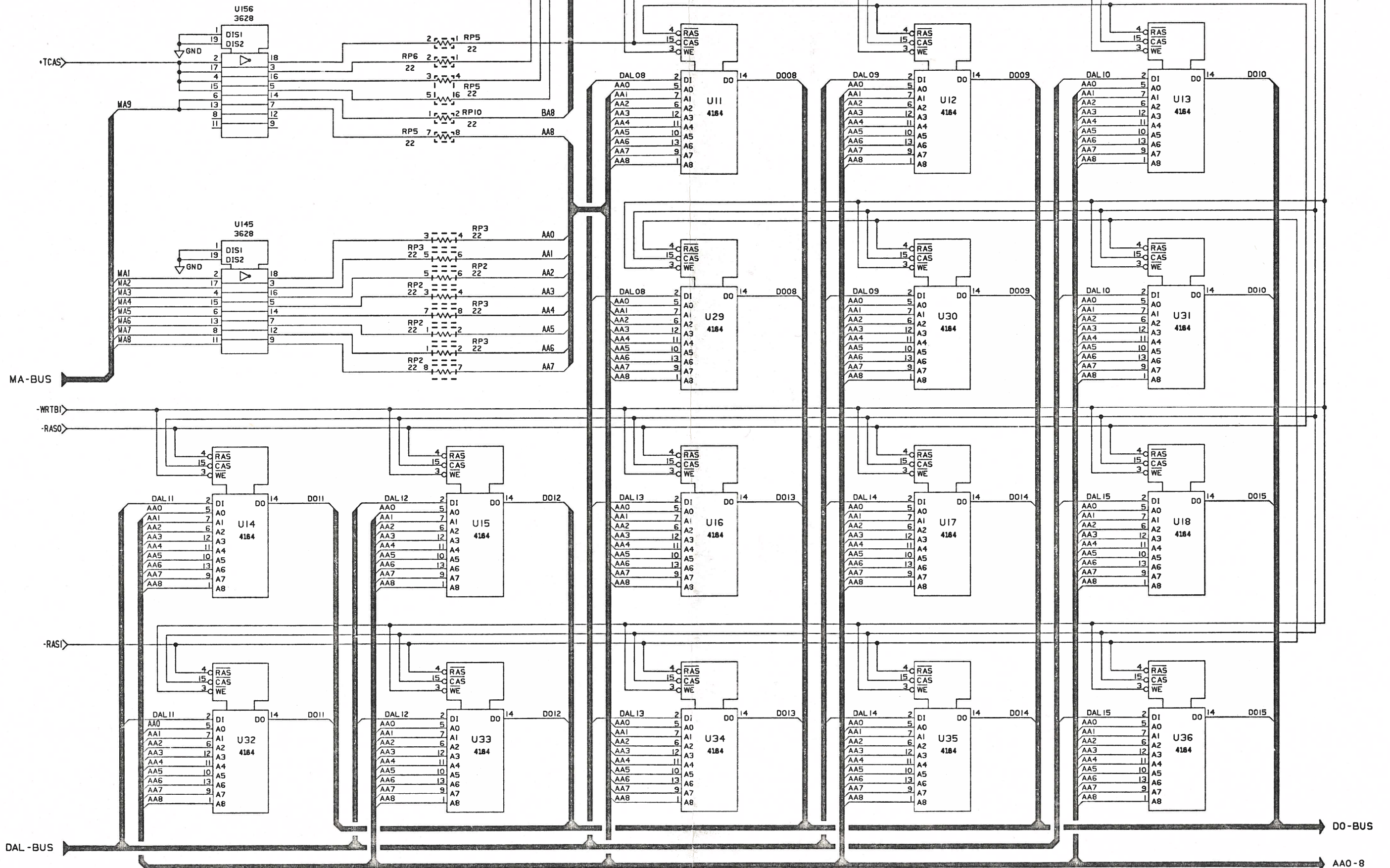
DFMPI
DFMPO

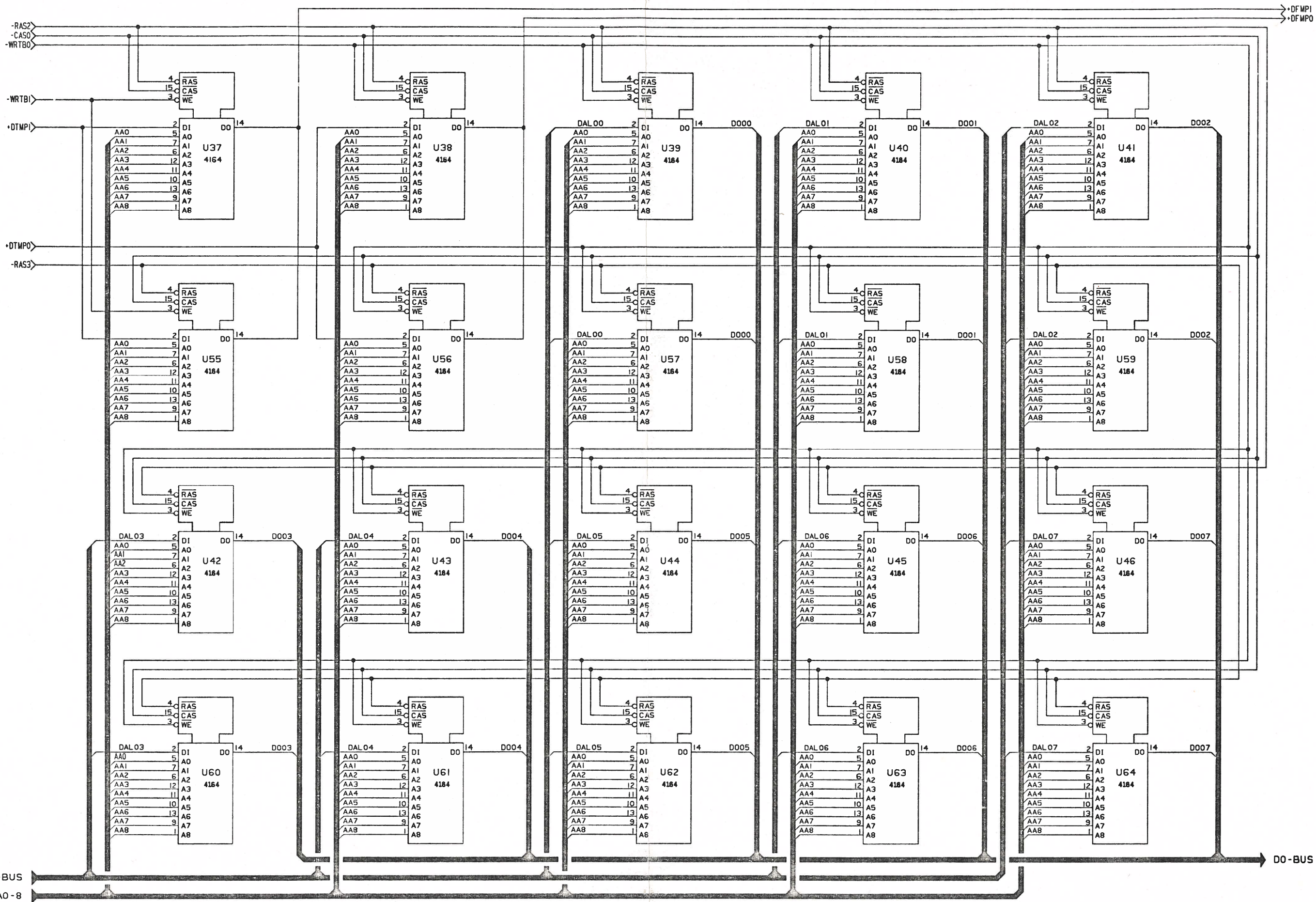


DAL-BUS
AA0-8

DO-BUS

BAB
-CAS1
-CAS2
-CAS3
-CAS0



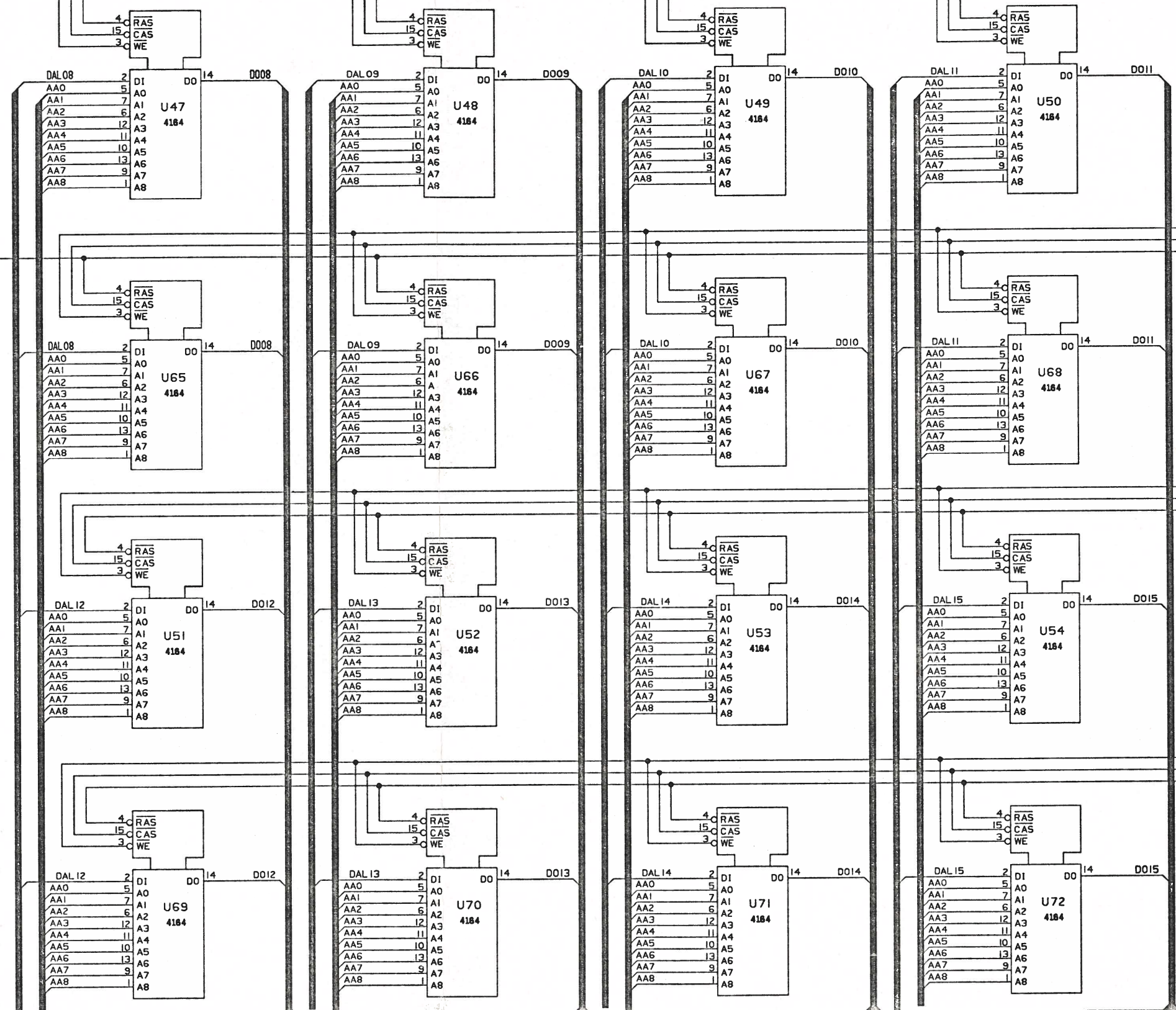


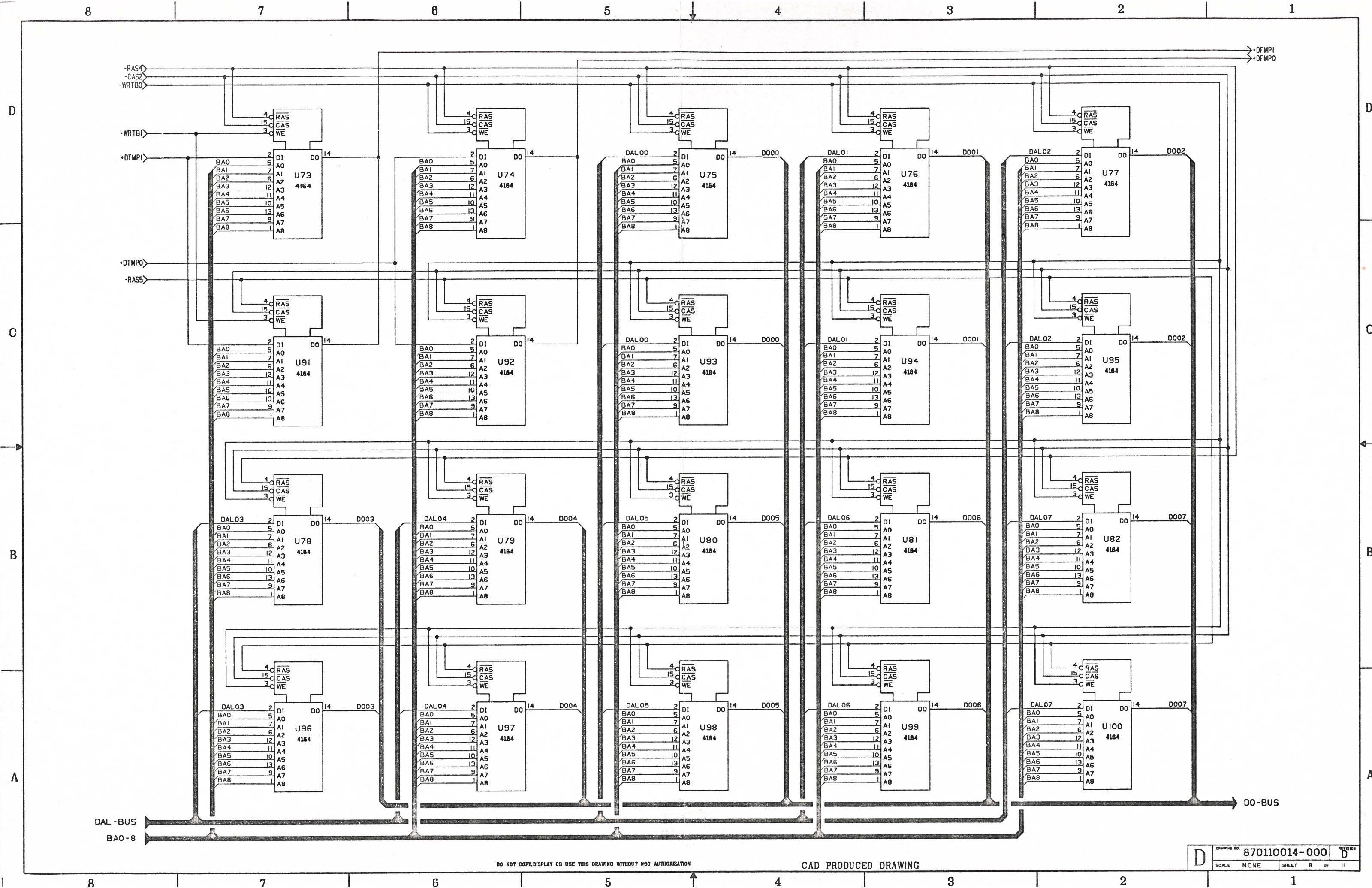
-RAS2
-CAS
-WR TBI

-RAS3

DAL - BUS
AAO - 8

DO - BUS

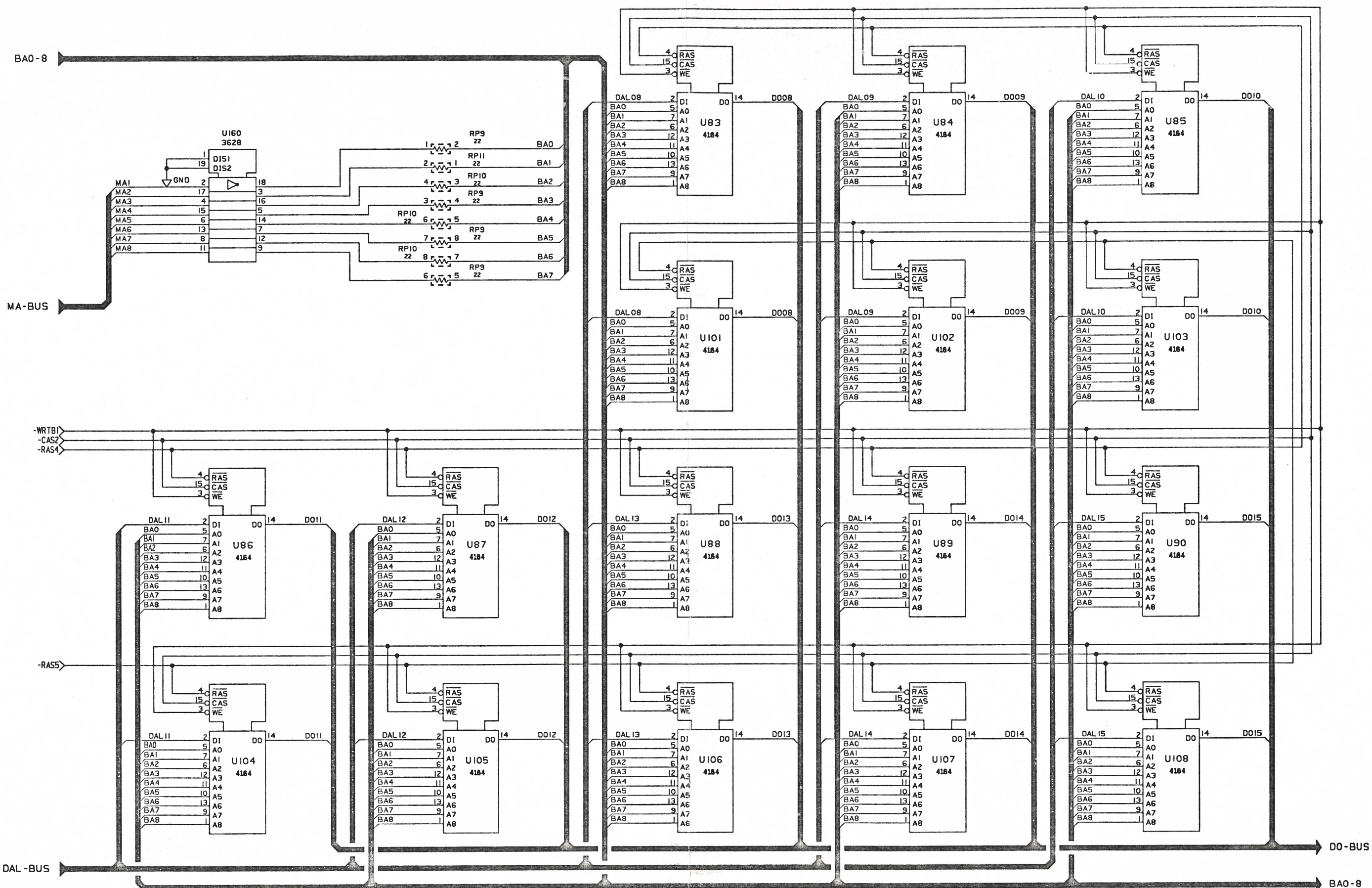




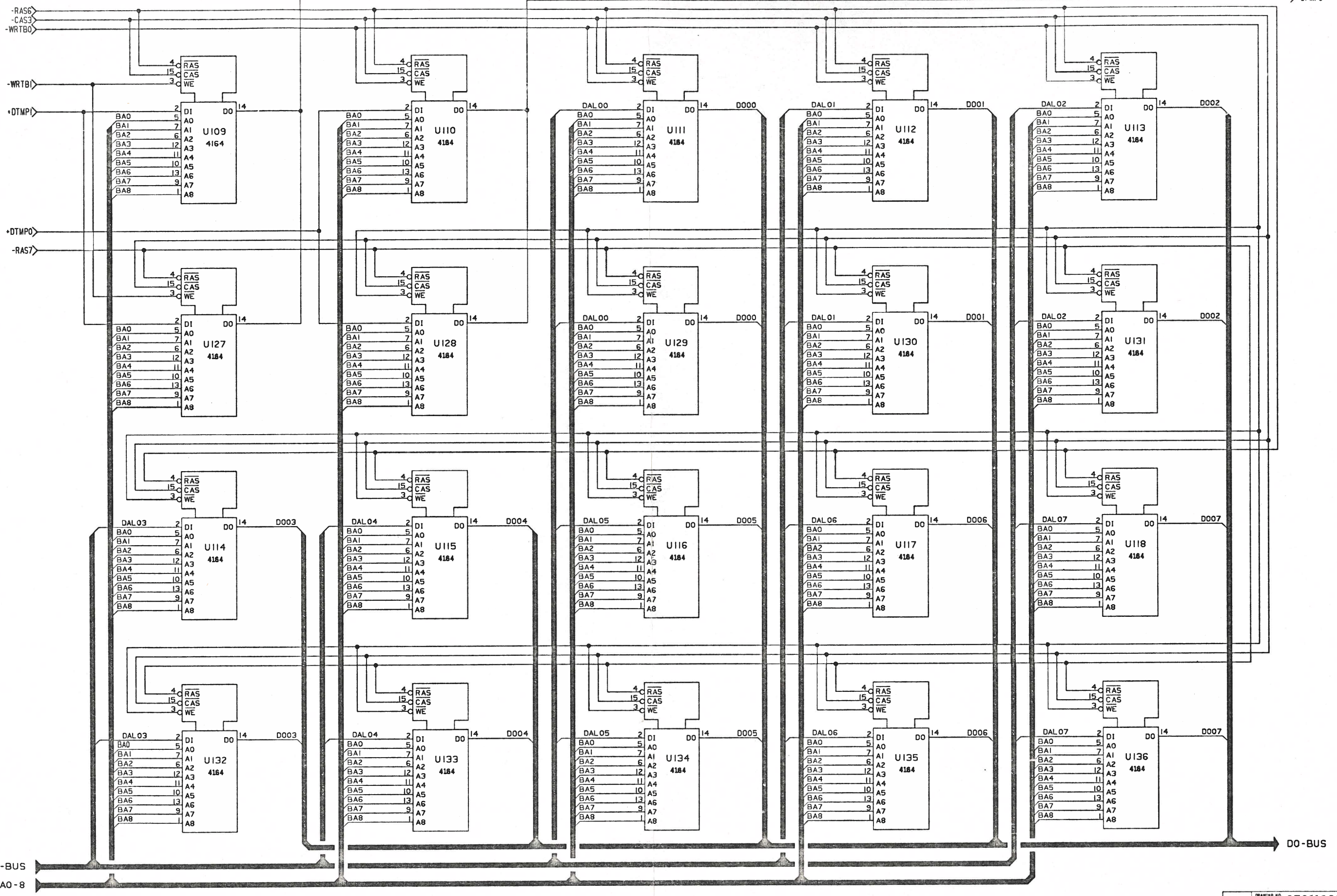
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CAD PRODUCED DRAWING

D	DRAWING NO.	870110014-000	REVISION	D
	SCALE	NONE	SHEET	8 OF 11



DFMP1
DFMPO



D

D

C

C

B

B

A

A

DAL - BUS
BA0 - 8

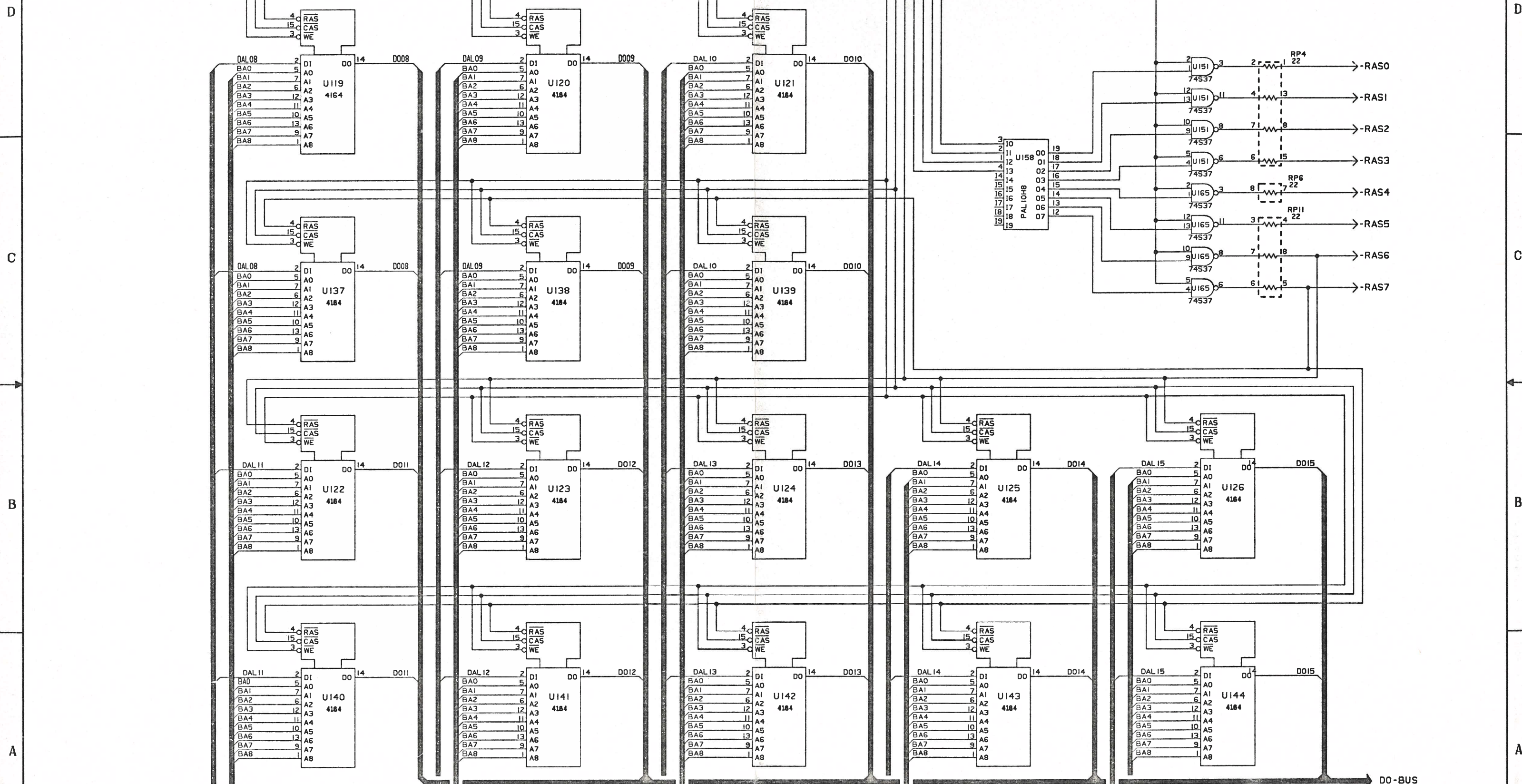
DO - BUS

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CAD PRODUCED DRAWING

D	DRWG NO. 870110014-000	REVISION
	SCALE NONE	SHEET 10 OF 11

•TRAS
-RS0
-RS1
-RS2
•REFH
-WRTB
-CAS3



DAL - BUS
BA0 - 8

DO - BUS